

XA-9415

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hirotaka NISHIZAWA et al.

Appln. No.: 09/756,867

Group Art Unit: 2876

Filed: January 10, 2501

Examiner: J. Fureman

For: IC CABO

AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action mailed April 15, 2003, please amend the above-identified patent application as indicated below.

IN THE TITLE:

Change the title to read:

--INTEGRATED CIRCUIT CARD HAVING STAGGERED SEQUENCES
OF CONNECTOR TERMINAL --.

- 1 /2. (Amended) An integrated circuit (IC) card
- 2 comprising:
- 3 a card substrate including,
- 4 a semiconductor integrated circuit chip mounted
- 5 thereon; and
- a plurality of connector terminals formed
- 7 thereon;
- 8 said connector terminals being exposed from a casing;
- 9 wherein said connector terminals include an
- 10 arrangement of first and second sequences formed back and
- 11 forth as viewed in an IC card inserting direction, and an
- 12 arrangement of terminal-to-terminal areas in the first
- 13 sequence and an arrangement of terminal-to-terminal areas
- 14 in the second sequence are shifted from one another in a
- 15 sequence direction, and
- 16 wherein the first sequence includes a source voltage
- 17 supply terminal, and the second sequence is devoid of
- 18 connector terminals at a position adjacent to the source
- 19 voltage supply terminal and at positions adjacent to
- 20 terminal-to-terminal areas at opposite sides of the source
- 21 voltage supply terminal.